Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (currently amended) A damascene method, comprising:

- (a) providing a substrate;
- (b) depositing a composite barrier/etch stop layer comprised of a lower silicon carbide (SiC) layer and an upper oxygen doped SiC layer on said substrate;
 - (c) forming a first dielectric layer on said upper oxygen doped SiC layer;
 - (d) forming an opening with sidewalls and a bottom in said first dielectric layer;
- (e) removing said composite barrier/etch stop layer that is exposed at the bottom of said opening;
- (f) depositing a conformal diffusion barrier layer on the sidewalls and bottom of said opening; and
- (g) depositing a metal layer on the conformal diffusion barrier layer that fills said opening.

Claim 2 (original) The method of claim 1 wherein the substrate is comprised of a conductive layer with a top surface and said opening exposes a portion of the top surface of the conductive layer.

Claim 3 (original) The method of claim 2 wherein the conductive layer and the metal layer are comprised of copper.

Claim 4 (original) The method of claim 1 wherein the diffusion barrier layer is comprised of Ta, TaN, TaSiN, Ti, TiN, W, or WN and has a thickness in the range of about 50 to 300 Angstroms.

Claim 5 (original) The method of claim **1** wherein said first dielectric layer is comprised of Black DiamondTM, CORALTM, fluorine doped SiO₂, hydrogen silsesquioxane (HSQ), methyl silsesquioxane (MSQ), a fluorinated polyimide, a polyarylether, or benzocyclobutene.

Claim 6 (currently amended) The method of claim 1 wherein the lower SiC layer has a thickness from about 50 to 150 Angstroms and the <u>upper</u> oxygen doped SiC layer has a thickness between about 50 and 1000 Angstroms.

Claim 7 (currently amended) The method of claim 1 wherein the <u>upper</u> oxygen doped SiC layer is deposited by a PECVD process that includes an oxygen flow rate from about 20 to 200 standard cubic centimeters per minute (sccm), a helium flow rate of about 700 to 1000 sccm, a trimethylsilane or tetramethylsilane flow rate of about 280 to 350 sccm, a substrate temperature of from 300°C to 400°C, a chamber pressure of 2 to 8 Torr, and a RF power of about 100 to 1000 Watts that is generated with a RF frequency of 13.56 MHz.

Claim 8 (original) The method of claim **7** wherein the substrate temperature is 350°C, the RF power is 460 Watts, chamber pressure is 3.5 Torr, the O₂ flow rate is about 30 sccm, the He flow rate is about 800 sccm, and the trimethylsilane flow rate is about 320

sccm to give an oxygen doped SiC deposition rate in the range of 850 to 950 Angstroms per minute.

Claim 9 (currently amended) The method of claim **7** wherein the <u>upper</u> oxygen doped SiC layer has a silicon content from about 25 to 35 atomic weight %, a carbon content of about 17 to 25 atomic wt. %, an oxygen content of about 5 to 15 atomic wt. %, and a hydrogen content from about 20 to 40 atomic wt. %.

Claim 10 (currently amended) The method of claim **7** wherein the <u>upper</u> oxygen doped SiC layer has a good etch selectivity to Black DiamondTM or CORALTM of about 1:6 to 1:10 in a plasma etch comprised of C₄F₈ and Ar gases.

Claim 11 (currently amended) The method of claim 1 further comprised of treating said <u>upper</u> oxygen doped SiC layer with a N_2 , He, or Ar plasma before said first dielectric layer is formed.

Claim 12 (original) The method of claim 1 further comprised of a chemical mechanical polish process to planarize said metal layer.

Claim 13 (currently amended) A dual damascene method, comprising:

- (a) providing a substrate with a conductive layer formed within a first dielectric layer, said conductive layer has an exposed top surface that is coplanar with the top surface of said substrate;
- (b) depositing a barrier/etch stop layer comprised of a lower silicon carbide (SiC) layer and an upper first oxygen doped SiC layer on said substrate;

- (c) forming a second dielectric layer on said upper first oxygen doped SiC layer;
- (d) depositing a second oxygen doped SiC etch stop layer on said second dielectric layer;
 - (e) forming a third dielectric layer on said second oxygen doped SiC layer;
- (f) forming an opening comprised of a via that exposes said conductive layer and a trench aligned above said via wherein the via has sidewalls and a bottom and extends through said second and third dielectric layers, second oxygen doped SiC layer, and through the composite barrier/etch stop layer and wherein the trench has sidewalls and a bottom and is formed in the third dielectric layer; and
- (g) depositing a conformal diffusion barrier layer on the sidewalls and bottom of said trench and via and depositing a metal layer on said conformal diffusion barrier layer that fills said trench and via.

Claim 14 (original) The method of claim 13 further comprised of forming a cap layer on the third dielectric layer and wherein the trench is aligned above a via that exposes the conductive layer and said trench is formed in the cap layer and third dielectric layer.

Claim 15 (original) The method of claim 13 wherein said conductive layer and metal layer are comprised of copper.

Claim 16 (original) The method of claim **13** wherein the diffusion barrier layer is comprised of Ta, TaN, TaSiN, Ti, TiN, W, or WN and has a thickness in the range of about 50 to 300 Angstroms.

Claim 17 (original) The method of claim **13** wherein said first, second, and third dielectric layers are a low k dielectric material comprised of Black DiamondTM, CORALTM, fluorine doped SiO₂, hydrogen silsesquioxane (HSQ), methyl silsesquioxane (MSQ),a fluorinated polyimide, a polyarylether, or benzocyclobutene.

Claim 18 (original) The method of claim 13 wherein said second and third dielectric layers have a thickness in the range of about 1000 to 10000 Angstroms.

Claim 19 (currently amended) The method of claim **14** wherein the SiC in said barrier/etch stop lower silicon carbide (SiC) layer has a thickness from about 50 to 150 Angstroms and the <u>upper</u> first <u>oxygen doped SiC layer</u> and second oxygen doped SiC layers have a thickness between about 50 and 1000 Angstroms.

Claim 20 (currently amended) The method of claim 13 wherein said <u>upper</u> first <u>oxygen doped SiC layers</u> and second oxygen doped SiC layers are deposited by a PECVD process that includes an oxygen flow rate from about 20 to 200 sccm, a helium flow rate of about 700 to 1000 sccm, a trimethylsilane or tetramethylsilane flow rate of about 280 to 350 sccm, a substrate temperature of from 300°C to 400°C, a chamber pressure of 2 to 8 Torr, and a RF power of about 100 to 1000 Watts that is generated with a RF frequency of 13.56 MHz.

Claim 21 (original) The method of claim **20** wherein the substrate temperature is 350° C, the RF power is 460 Watts, chamber pressure is 3.5 torr, the O₂ flow rate is 30 sccm, the He flow rate is 800 sccm, and the trimethylsilane flow rate is 320 sccm to give an oxygen doped SiC deposition rate in the range of 850 to 950 Angstroms per minute.

Claim 22 (currently amended) The method of claim 20 wherein the <u>upper</u> first <u>oxygen</u> doped SiC layer and second oxygen doped SiC layers have an etch selectivity to Black DiamondTM or CORALTM of about 1:6 to 1:10 in a plasma etch comprised of C_4F_8 and Ar gases.

Claim 23 (currently amended) The method of claim 21 wherein the composition of the upper first oxygen doped SiC layer and second oxygen doped SiC layers has a silicon content from about 25 to 35 atomic weight %, a carbon content of about 17 to 25 atomic wt. %, an oxygen content of about 5 to 15 atomic wt. %, and a hydrogen content from about 20 to 40 atomic wt. %.

Claim 24 (currently amended) The method of claim 13 further comprised of treating the <u>upper</u> first oxygen doped SiC layer with a N_2 , Ar, or He plasma prior to depositing the second dielectric layer and treating the second oxygen doped SiC layer with a N_2 , Ar, or He plasma prior to depositing the third dielectric layer.

Claims 25 - 37 are cancelled.